

**AMENDMENTS TO THE CLAIMS**

The listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A method for loading software on a plurality of processors in a heterogeneous processor environment, said method comprising:  
 retrieving a file using a first processor;  
extracting a processor identifier from the file, the processor identifier corresponding to the file;  
~~detecting a processor identifier that corresponds to the file;~~  
 determining whether to load the file on a second processor based upon whether the processor identifier corresponds to the second processor ~~the processor identifier;~~ and  
 loading the file onto the second processor in response to the determination.
2. (Original) The method as described in claim 1 further comprising:  
 executing a program on the first processor;  
 loading a runtime loader onto the first processor in response the execution; and  
 performing the retrieving, detecting, and the determining using the runtime loader.
3. (Original) The method as described in claim 1 wherein the file is an executable file.
4. (Original) The method as described in claim 3 further comprising:  
 sending a plug-in to the second processor using the first processor, the plug-in corresponding to the file;

sending data to the second processor using the first processor, the data corresponding to the plug-in; and  
processing the data with the plug-in using the second processor.

5. (Original) The method as described in claim 3 further comprising:  
retrieving a plug-in using the second processor, the plug-in corresponding to the file;  
retrieving data using the second processor, the data corresponding to the plug-in;  
and  
processing the data with the plug-in using the second processor.
6. (Currently Amended) The method as described in claim 3 wherein the executable file is in a file format, and wherein the file format is selected from the group consisting of an ELF Executable and Linking format, an XCOFF Extended Common Object File format, and a PECOFF Portable Executable Common Object File format.
7. (Original) The method as described in claim 1 wherein the processor identifier is a machine type, the determining further comprising:  
extracting the machine type from the file; and  
comparing the machine type to a plurality of machine types.
8. (Original) The method as described in claim 1 wherein the file is part of a combined file, and wherein the processor type corresponds to one or more section headers from a plurality of section headers.
9. (Original) The method as described in claim 1 wherein the file is part of a combined file, and wherein the combined file includes one or more processor identifiers that correspond to the first processor.

10. (Original) The method as described in claim 1 wherein the first processor is a processing unit and wherein the second processor is a synergistic processing unit.
11. (Currently Amended) An information handling system comprising:
  - a plurality of processors in a heterogeneous processor environment;
  - a memory accessible by the plurality of processors;
  - one or more nonvolatile storage devices accessible by the plurality of processors;
  - and
  - a software loading tool for loading software on a plurality of processors, the software loading tool comprising software code effective to:
    - retrieve a file using a first processor from one of the nonvolatile storage devices;
    - extract a processor identifier from the file, the processor identifier corresponding to the file;
    - ~~detect a processor identifier using the first processor that corresponds to the file;~~
    - determine whether to load the file on a second processor based upon whether the processor identifier corresponds to the second processor the processor identifier; and
    - load the file onto the second processor in response to the determination.
12. (Original) The information handling system as described in claim 11 wherein the software code is further effective to:
  - execute a program on the first processor;
  - load a runtime loader onto the first processor in response the execution; and

perform the retrieving, detecting, and the determining using the runtime loader located on the first processor.

13. (Original) The information handling system as described in claim 11 wherein the file is an executable file.
14. (Original) The information handling system as described in claim 13 wherein the software code is further effective to:  
  
send a plug-in to the second processor using the first processor, the plug-in corresponding to the file;  
  
send data to the second processor using the first processor, the data corresponding to the plug-in; and  
  
process the data with the plug-in using the second processor.
15. (Original) The information handling system as described in claim 13 wherein the software code is further effective to:  
  
retrieve a plug-in using the second processor from one of the nonvolatile storage devices, the plug-in corresponding to the file;  
  
retrieve data using the second processor from one of the nonvolatile storage devices, the data corresponding to the plug-in; and  
  
process the data with the plug-in using the second processor.
16. (Currently Amended) The information handling system as described in claim 13 wherein the executable file is in a file format, and wherein the file format is selected from the group consisting of an ELF Executable and Linking format, an XCOFF Extended Common Object File format, and a PE/COFF Portable Executable Common Object File format.

17. (Original) The information handling system as described in claim 11 wherein the processor identifier is a machine type, and wherein the software code is further effective to:  
  
extract the machine type from the file; and  
  
compare the machine type to a plurality of machine types.
18. (Original) The information handling system as described in claim 11 wherein the file is part of a combined file, and wherein the processor type corresponds to one or more section headers from a plurality of section headers.
19. (Original) The information handling system as described in claim 11 wherein the file is part of a combined file, and wherein the combined file includes one or more processor identifiers that correspond to the first processor.
20. (Original) The information handling system as described in claim 11 wherein the first processor is a processing unit and wherein the second processor is a synergistic processing unit.
21. (Currently Amended) A computer program product comprising computer readable code stored in computer memory, the computer readable code being effective to: A computer program product stored on a computer operable media for loading software on a plurality of processors in a heterogeneous processor environment, said computer program product comprising:  
  
means for retrieving retrieve a file using a first processor;  
  
extract a processor identifier from the file, the processor identifier corresponding to the file;  
  
means for detecting a processor identifier that corresponds to the file;

~~means for determining~~ determine whether to load the file on a second processor based upon whether the processor identifier corresponds to the second processor ~~the processor identifier~~; and

~~means for loading~~ load the file onto the second processor in response to the determination.

22. (Currently Amended) The computer program product as described in claim 21 wherein the computer readable code is further effective to ~~further comprising:~~

~~means for executing~~ execute a program on the first processor;

~~means for loading~~ load a runtime loader onto the first processor in response the execution; and

~~means for performing~~ perform the retrieving, detecting, and the determining using the runtime loader.

23. (Original) The computer program product as described in claim 21 wherein the file is an executable file.

24. (Currently Amended) The computer program product as described in claim 23 wherein the computer readable code is further effective to ~~further comprising:~~

~~means for sending~~ send a plug-in to the second processor using the first processor, the plug-in corresponding to the file;

~~means for sending~~ send data to the second processor using the first processor, the data corresponding to the plug-in; and

~~means for processing~~ process the data with the plug-in using the second processor.

25. (Currently Amended) The computer program product as described in claim 23 wherein the computer readable code is further effective to ~~further comprising:~~

~~means for retrieving~~ retrieve a plug-in using the second processor, the plug-in corresponding to the file;

~~means for retrieving~~ retrieve data using the second processor, the data corresponding to the plug-in; and

~~means for processing~~ process the data with the plug-in using the second processor.

26. (Currently Amended) The computer program product as described in claim 23 wherein the executable file is in a file format, and wherein the file format is selected from the group consisting of an ELF Executable and Linking format, an XCOFF Extended Common Object File format, and a PECOFF Portable Executable Common Object File format.
27. (Currently Amended) The computer program product as described in claim 21 wherein the processor identifier is a machine type, the means for determining further comprising:
 

~~means for extracting~~ extract the machine type from the file; and

~~means for comparing~~ extract the machine type to a plurality of machine types.
28. (Original) The computer program product as described in claim 21 wherein the file is part of a combined file, and wherein the processor type corresponds to one or more section headers from a plurality of section headers.
29. (Original) The computer program product as described in claim 21 wherein the file is part of a combined file, and wherein the combined file includes one or more processor identifiers that correspond to the first processor.
30. (Original) The computer program product as described in claim 21 wherein the first processor is a processing unit and wherein the second processor is a synergistic processing unit.